

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(encoding instructions for target processor to achieve intended results with optimize\$5 with operation with code) and encoding with instructions with target with processor with achieve with intended with results	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/06 11:44
L2	15	(generat\$4 same target same (instruction\$4 or code or program or software) same system) and exection	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/06 11:44
L3	0	717/140-151.ccls. and (generat\$4 same target same (instruction\$4 or code or program or software) same system) and exection	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/06 11:44
L4	1	717/140-151.ccls. and (encoding instructions for target processor to achieve intended results with optimize\$5 with operation with code) and encoding with instructions with target with processor with achieve with intended with results	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/06 11:44
L5	0	717/140.ccls. and (generat\$4 same target same (instruction\$4 or code or program or software) same system) and (multi adj system) and execut\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/06 11:45
L6	0	"717.140-151.ccls" and (generat\$4 same target same (instruction\$4 or code or program or software) same system) and (multi adj system) and execut\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/06 11:46
S98	18	(generat\$4 same target same (instruction\$4 or code or program or software) same system) and (multi adj system) and execut\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/06 11:45

S10 4	0	717/140.ccls. and (generat\$4 same target same (instruction\$4 or code or program or software) same system) and (multi adj system) and execut\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/06 11:45
S11 1	1	(encoding instructions for target processor to achieve intended results with optimize\$5 with operation with code) and encoding with instructions with target with processor with achieve with. intended with results	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/06 11:42



Key: IEEE JNL = IEEE Journal or Magazine, IEE JNL = IEE Journal or Magazine, IEEE CNF = IEEE Conference, IEE CNF = IEE Conference, IEEE STD = IEEE Standard

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3. Instruction-set modelling for ASIP code generation

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4. Instruction scheduling for instruction level parallel processors

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6. A simple tree pattern matching algorithm for code generator

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8. Instruction selection, resource allocation, and scheduling in the AVIV retargetable code generator

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9. Generating efficient loop code for programmable DSPs

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11. Industrial experience using rule-driven retargetable code generation for multimedia applications

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